

SUPER TWISTED NEMATIC (STN) LIQUID CRYSTAL DISPLAY (LCD) DRIVER AND DRIVING METHOD THEREOF

BACKGROUND OF THE INVENTION

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This application claims priority from Korean Patent Application No. 2002-71391, filed on 16 November 2002, in the Korean Intellectual Property Office, the contents of which are incorporated herein in their entirety by reference.

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1. Field of the Invention

The present invention relates to a super twisted nematic (STN) liquid crystal display (LCD) driver, and more particularly, to an STN LCD driver using a frame rate control (FRC) technique as a driving method.

2. Description of the Related Art

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In a super twisted nematic (STN) liquid crystal display (LCD) driver using an iAPT or APT method, pulse width modulation, frame rate control (FRC), or a combination thereof is widely used to present gray scales and colors.

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FIG. 1 is a table showing levels of a super twisted nematic (STN) liquid crystal driving voltage in response to a level of a liquid crystal polarity inversion signal M in a FRC method.

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The liquid crystal polarity inversion signal M is a periodic signal used to prevent liquid crystal from solidifying. That is, the level of the liquid crystal polarity inversion signal M has to be periodically inverted so as to prevent the liquid crystal from solidifying. After the level of the liquid crystal polarity inversion signal M is inverted, levels of a segment voltage VSEG driving a column electrode of an STN liquid crystal and a com voltage VCOM driving a row electrode of the STN liquid crystal are also inverted.

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Referring to FIG. 1, if the level of the liquid crystal polarity inversion signal M is high, the selection voltage level of the segment voltage VSEG is V0. If the level of the liquid crystal polarity inversion signal M is low, the non-selection voltage level of the segment voltage VSEG is Vss.

A relationship between the voltage levels of the segment voltage VSEG can be expressed by $V_0 > V_2 > V_3 > V_{ss}$.

If the level of the liquid crystal polarity inversion signal M is high, a non-selection voltage level of the segment voltage VSEG is V_2 . If the level of the liquid crystal polarity inversion signal M is low, the non-selection voltage level of the segment voltage VSEG is V_3 .

Similarly to the voltage level of the segment voltage VSEG, the voltage level of the com voltage VCOM is also inverted in response to the level of the liquid crystal polarity inversion signal M.

If the level of the segment voltage VSEG or the com voltage VCOM are the selection voltage level, the liquid crystal is turned on and presented as black. If the level of the segment voltage VSEG or the com voltage VCOM are the non-selection voltage level, the liquid crystal is turned off and presented as white.

Yet, if the non-selection voltage levels V_2 and V_3 of the segment voltage VSEG are not used the same number of times in the FRC method, cross talk occurs in the liquid crystal.

FIGS. 2A through 2D are waveforms of a segment voltage VSEG in a conventional STN LCD driving method using a 3FRC method.

A frame in an nFRC method is comprised of n of sub frames. Thus, a frame in the 3FRC method is comprised of three sub frames. Pixels at the right side of FIGS. 2A through 2D present different gray scales of the liquid crystal.

That is, FIG. 2A shows the liquid crystal presenting black, FIG. 2B shows the liquid crystal presenting dark gray, FIG. 2C shows the liquid crystal presenting light gray, and FIG. 2D shows the liquid crystal presenting white.

At each sub frame, the level of the liquid crystal inversion signal M is inverted. Thus, the same signal is repeated once every six sub frames.

In the first sub frame, the level of the liquid crystal polarity inversion signal M is high. In the second sub frame, the level of the liquid crystal polarity inversion signal M is inverted, i.e., low. Accordingly, in order to present the liquid crystal as dark gray, two sub frames have to present black, and one sub frame has to present white as shown in FIG. 2B.

Referring to FIG. 2B, in order to present the liquid crystal as dark gray, the level of the segment voltage VSEG is V0 which is the selection voltage level in the first sub frame where the level of the liquid crystal inversion signal M is high. The level of the segment voltage VSEG is Vss which is the non-selection level in the second sub frame where the level of the liquid crystal inversion signal M is low. The level of the segment voltage VSEG is V2 which is the non-selection level in the third sub frame where the level of the liquid crystal inversion signal M is high. When such sub frames are consecutively repeated by a period over 60Hz, human eyes recognize these as dark gray. Here, since the selection voltage levels V0 and Vss and the non-selection voltage levels V2 and V3 are used the same number of times in total 6 sub frames, cross talk does not occur in the liquid crystal.

FIGS. 3A through 3E are waveforms of a segment voltage VSEG in a conventional STN LCD driving method using a 4FRC method.

A frame is comprised of four sub frames. Referring to FIG. 3B, only V3 is used as the non-selection voltage level, and V2 is not used as the non-selection voltage level. In addition, the selection voltage level V0 is more frequently used than the other non-selection voltage level Vss.

Referring to FIG. 3C, the selection voltage levels V0 and Vss are used the same number of times, and the non-selection voltage levels V2 and V3 are used the same number of times also.

Referring to FIG. 3D, the non-selection voltage level V3 is more frequently used than the non-selection other voltage level V2. Only V0 is used as the selection voltage level, and Vss is not used as the selection voltage level.

Thus, cross talk occurs in the liquid crystal in the waveforms of FIGS. 3B and 3D, and does not occur in the liquid crystal in the waveform of FIG. 3C.

FIGS. 4A through 4E are waveforms of a segment voltage in an STN LCD driving method using an N-line inversion method.

The N-line inversion method is used to prevent cross talk shown in FIGS. 3A through 4E from occurring. The N-line inversion method includes dividing one frame into N sub frames and inverting the level of the liquid crystal inversion signal M in each sub frame.

Referring to FIGS. 4A through 4E, the selection voltage level or the non-selection voltage level are used the same number of times. Therefore, cross talk can be prevented from occurring in the liquid crystal.

However, the N-line inversion method increases the number of transitions of the level of the segment voltage VSEG. Thus, power consumption also increases. That is, the nFRC method has a disadvantage in that cross talk occurs, and the N-line inversion method has a disadvantage of increased power consumption.

SUMMARY OF THE INVENTION

The present invention provides a super twisted nematic (STN) liquid crystal display (LCD) driver which makes it possible to use selection voltage levels and non-selection voltage levels the same number of times and reduces the number of transitions of the level of a segment voltage.

The present invention also provides a driving method of an STN LCD driver which makes it possible to use selection voltage levels and non-selection voltage levels the same number of times and reduces the number of transitions of a level of a segment voltage.

According to a first embodiment of an aspect of the present invention, there is provided a super twisted nematic (STN) liquid crystal display (LCD) driver comprising a sub frame counter, an N clock counter, a frame counter, and a liquid crystal polarity inversion signal generator. The sub frame counter counts the number of sub frames in response to a clock signal and generates a sub frame flag signal every time each sub frame is counted. The N clock counter receives an N-line signal and generates an N-line flag signal every time when the number of N-line counted is N in response to the clock signal. The frame counter receives a frame rate control (FRC) selection signal, counts the number of the sub frame flag signal, and generates a frame flag signal every time the number of the sub frame flag signal counted is n. The liquid crystal polarity inversion signal generator receives one of the sub frame flag signal, the N-line flag signal, and the frame flag signal in response to the FRC selection signal, and generates a liquid crystal polarity inversion signal that inverts a polarity of an STN LCD.

In one embodiment, the STN LCD driver further comprises a column driver and a row driver. The column driver receives data and generates a segment voltage that

drives a column electrode of the STN LCD in response to a level of the liquid crystal polarity inversion signal. The row driver receives a row selection signal and generates a com voltage that drives a row electrode of the STN LCD in response to the level of the liquid crystal polarity inversion signal.

5 The FRC selection signal has information on whether a driving method of the STN LCD is an nFRC method, where n is a natural number. The N-line signal can have information used to divide a frame into N sub frames, where N is a natural number.

10 According to another aspect of the present invention, there is provided a driving method of a super twisted nematic (STN) liquid crystal display (LCD) driver. The driving method comprises (a) counting the number of sub frames in response to a clock signal and generating a sub frame flag signal every time each frame is counted, (b) receiving an N-line signal and generating an N-line flag signal in response to input of the clock signal every time the number of N-line counted is N in response to the clock signal,
15 (c) receiving a frame rate control (FRC) selection signal, counting the number of sub frame flag signals, and generating a frame flag signal every time the number of sub frame flag signals counted is n , and (d) selecting one of the sub frame flag signal, the N-line flag signal, and the frame flag signal in response to the FRC selection signal, and generating a liquid crystal polarity inversion signal that inverts a polarity of the STN
20 CLD.

 The driving method of the STN LCD driver further comprises (e) receiving data and generating a segment voltage that drives a column electrode of the STN LCD in response to the level of the liquid crystal polarity inversion signal and (f) receiving a row selection signal and, in response to the level of the liquid crystal polarity inversion signal,
25 generating a com voltage that drives a row electrode of STN LCD.

 According to another aspect of the present invention, there is provided a driving method of a super twisted nematic (STN) liquid crystal display (LCD) driver. The driving method comprises (a) determining whether a frame rate control (FRC) selection signal is in accordance with an nFRC method, (b) counting the number of sub frames, and (c) generating a liquid crystal polarity inversion signal that inverts a polarity of the
30 STN LCD if the number of sub frames is n .

The driving method of the STN LCD driver further comprises (d) receiving data and, in response to the level of the liquid crystal polarity inversion signal, generating a segment voltage that drives a column electrode of the STN LCD and (f) receiving a row selection signal and, in response to the level of the liquid crystal polarity inversion signal, generating a com voltage that drives a row electrode of the STN LCD.

In one embodiment, N sub frames constitute one frame.

According to a third embodiment of the present invention, there is provided a driving method of a super twisted nematic (STN) liquid crystal display (LCD) driver using an nFRC method, wherein a polarity of the STN LCD is inverted in each frame. One frame is comprised of n sub frames.

BRIEF DESCRIPTION OF THE DRAWINGS

The foregoing and other objects, features and advantages of the invention will be apparent from the more particular description of a preferred embodiment of the invention, as illustrated in the accompanying drawings in which like reference characters refer to the same parts throughout the different views. The drawings are not necessarily to scale, emphasis instead being placed upon illustrating the principles of the invention.

FIG. 1 is a table showing the levels of a super twisted nematic (STN) liquid crystal driving voltage in response to a level and selection or non-selection of a liquid crystal polarity inversion signal M used in a frame rate control (FRC) method.

FIGS. 2A through 2D are waveforms of a segment voltage VSEG used in a conventional STN LCD driving method using a 3FRC method.

FIGS. 3A through 3E are waveforms of a segment voltage VSEG used in a conventional STN LCD driving method using a 4FRC method.

FIGS. 4A through 4E are waveforms of a segment voltage used in an STN LCD driving method using an N-line inversion method.

FIG. 5 is a block diagram of an STN LCD driver according to an embodiment of the present invention.

FIGS. 6A through 6D are waveforms of a segment voltage in the STN LCD driver of FIG. 5 using a 3FRC method.

FIGS. 7A through 7E are waveforms of a segment voltage in the STN LCD driver of FIG. 5 using a 4FRC method.

FIG. 8 is a block diagram showing a driving method of an STN LCD driver according to an embodiment of the present invention.

5 FIG. 9 is a block diagram showing a driving method of an STN LCD driver further included in the method of FIG. 8.

FIG. 10 is a block diagram showing a driving method of an STN LCD driver according to another embodiment of the present invention.

10 FIG. 11 is a block diagram showing a driving method of an STN LCD driver further included in the method of FIG. 10.

DETAILED DESCRIPTION OF THE INVENTION

FIG. 5 is a block diagram of the STN LCD driver 500 according to an embodiment of the present invention.

15 Referring to FIG. 5, the STN LCD driver 500 includes a sub frame counter 510, an N clock counter 520, a frame counter 530, and a liquid crystal polarity inversion signal generator 540.

20 The sub frame counter 510 counts the number of sub frames in response to a clock signal CLK and generates a sub frame flag signal SFFLAG every time when each sub frame is counted.

The N clock counter 520 receives an N-line signal NS and generates an N-line flag signal NLFLAG every time when the number of lines counted is N in response to the clock signal CLK. The N-line signal NS has information used to divide a frame into N sub frames. Here, "N" is a natural number.

25 The frame counter 530 receives a frame rate control (FRC) selection signal FRCSEL, counts the number of sub frame flag signals SFFLAG, and generates a frame flag signal FFLAG every time when the number of sub frame flag signal SFFLAG counted is n. The FRC selection signal FRCSEL has information on whether a driving method of the STN LCD is an nFRC method. Here, "n" is a natural number.

30 The liquid crystal polarity inversion signal generator 540 receives one of the sub frame flag signal SFFLAG, the N-line flag signal NLFLAG, and the frame flag signal FFLAG in response to a selection signal MSEL and generates a liquid crystal polarity

inversion signal M which inverts a polarity of the liquid crystal.

The STN LCD driver 500 may further include a column driver 550 and a row driver 560. The column driver 550 receives data DATA and generates a segment voltage VSEG that drives a column electrode of the STN LCD in response to the liquid crystal polarity inversion signal M.

The row driver 560 receives a row selection signal RSEL and generates a com voltage VCOM that drives a row electrode of the STN LCD in response to the liquid crystal polarity inversion signal M.

The operation of the STN LCD driver 500 of FIG. 5 will be described with reference to a driving method of an STN LCD driver 800 of FIGS. 8 and 9.

In the conventional art, the level of the liquid crystal polarity inversion signal M is inverted at each sub frame to control the levels of the segment voltage VSEG and the com voltage VCOM so that the liquid crystal can be prevented from solidifying. In the present invention, the level of the liquid crystal polarity inversion signal M is inverted in each frame to control the levels of the segment voltage VSEG and the com voltage VCOM.

In response to the clock signal, the number of sub frames is counted, and a sub frame flag signal is generated every time when each sub frame is counted (step 810). The sub frame counter 510 counts the number of sub frames in response to the clock signal CLK.

If a driving method of the STN LCD driver is the nFRC method, a frame is comprised of n sub frames. The sub frame counter 510 generates the sub frame flag signal SFFLAG every time when each sub frame is counted. Therefore, in the nFRC method, the total number of the sub frame flag signal SFFLAG generated is n.

The N-line signal is received, and the N-line flag signal is generated every time when the number of N-line counted is N in response to the clock signal CLK (step 820). The N clock counter 520 receives the N-line signal NS and counts the N number of the N-line. The N-line signal NS has information used to divide a frame into the N sub frames. Here, "N" is a natural number. The N clock counter 520 generates the N-line flag signal NLFLAG every time when the number of N-lines counted is N.

A FRC selection signal is received, and the number of sub frame flag signal is

counted to generate a frame flag signal every time when the number of frame flag signal FFLAG counted is n (step 830). The frame counter receives the FRC selection signal FRCSEL and counts the number of sub frame flag signal SFFLAG.

The FRC selection signal FRCSEL has information on whether a driving method of the STN LCD is an nFRC method. Here, "n" is a natural number. If the driving method of the STN LCD is a 3FRC method, "n" is 3, thus a frame is comprised of three sub frames. Since the sub frame flag signal SFFLAG is generated every time when each sub frame is counted, the number of sub frame flag signal SFFLAG generated is 3.

If 3 sub frame flag signals SFFLAGs are generated, the frame counter 530 generates one frame flag signal FFLAG. That is, the frame counter 530 generates a frame flag signal FFLAG in each frame.

In response to the FRC selection signal FRCSEL, one of the sub frame flag signal, the N-line flag signal, and the frame flag signal FFLAG is selected, and the liquid crystal polarity inversion signal M, which inverts the polarity of the STN liquid crystal, is generated (step 840).

If the STN LCD is driven by the conventional nFRC method, the liquid crystal polarity inversion signal generator 540 receives the sub frame flag signal SFFLAG and inverts the level of the liquid crystal polarity inversion signal M.

If the STN LCD is driven by a general N-line inversion method, the liquid crystal polarity inversion signal generator 540 receives the N-line flag signal NLFLAG and inverts the level of the liquid crystal polarity inversion signal M.

However, in the present invention, it is possible to select the N-line flag signal NLFLAG or the sub frame flag signal SFFLAG by using the selection signal MSEL and select the frame flag signal FFLAG which inverts the level of the liquid crystal polarity inversion signal M in each frame.

If the frame flag signal FFLAG is selected, the level of the liquid crystal polarity inversion signal M is inverted in each frame. The selection signal MSEL is an externally inputted command.

Data are received, and a segment voltage, which drives a column electrode of the STN LCD, is generated in response to the level of the liquid crystal polarity inversion

signal M(step 850). Data DATA means data displayed on the liquid crystal. The column driver 550 receives data DATA and generates the segment voltage VSEG in response to the liquid crystal polarity inversion signal M.

The segment voltage VSEG is generated according to the table of FIG. 1. That is, in order to present the liquid crystal as dark, the level of the segment voltage VSEG has to be equal to the selection voltage level. Since the level of the liquid crystal polarity inversion signal M is inverted in each frame, the level of the segment voltage VSEG generated is V0 in one frame if the level of the liquid crystal polarity inversion signal M is high. Since the level of the liquid crystal polarity inversion signal M is low in the next frame, the level of the segment voltage VSEG generated is Vss.

A row selection signal is received, and a com voltage, which drives a row electrode of the STN liquid crystal, is generated in response to the level of the liquid crystal polarity inversion signal M (step 860). The row selection signal RSEL is a signal for selecting a row electrode to transmit the com voltage to the row electrode of the liquid crystal.

The row driver 560 receives the row selection signal RSEL and generates the com voltage VCOM in response to the level of the liquid crystal polarity inversion signal M. The com voltage VCOM is generated according to the table of FIG. 1.

FIGS. 6A through 6D are waveforms of a segment voltage in the STN LCD driver of FIG. 5 using a 3FRC method.

In FIG. 6, a waveform of the segment voltage VSEG using the conventional 3FRC method is indicated by a dotted line, and a waveform of the segment voltage VSEG according to the present invention is indicated by a solid line. In the conventional 3FRC method, the level of the liquid crystal polarity inversion signal M is inverted in each sub frame. However, in the present invention, the level of the liquid crystal polarity inversion signal M is inverted in every three sub frames, i.e., each frame.

Referring to FIG. 6A where the liquid crystal is presented as black, the level of the segment voltage VSEG maintains V0 in a first frame 1F where the level of the liquid crystal polarity inversion signal M is high. The level of the segment voltage VSEG maintains Vss in a second frame 2F where the level of the liquid crystal polarity inversion signal M is low.

Thus, in comparison to the waveform of the segment voltage VSEG in the conventional art, transitions between the voltage levels V0 and Vss decreases by 1/3. Therefore, power consumption due to transitions of the levels of the segment voltage VSEG can be reduced.

5 Referring to FIG. 6B where the liquid crystal presents dark gray and FIG. 6C where the liquid crystal is presented as light gray, the non-selection voltage levels V2 and V3 are used the same number of times. Referring to FIG. 6D where the liquid crystal is presented as white, transitions between the non-selection voltage levels V2 and V3 decreases by 1/3 compared to the waveform of the segment voltage VSEG in
10 the conventional art. Therefore, power consumption due to transitions between the levels of the segment voltage decreases.

FIGS. 7A through 7E are waveforms of a segment voltage in the STN LCD driver of FIG. 5 using a 4FRC method.

15 In FIG. 7, a waveform of the segment voltage VSEG using the conventional 4FRC method is indicated by a dotted line, and a waveform of the segment voltage VSEG according to the present invention is indicated by a solid line. In the conventional 4FRC method, the level of the liquid crystal polarity inversion signal M is inverted in each sub frame. However, in the present invention, the level of the liquid crystal polarity inversion signal M is inverted in every four sub frames, i.e., each frame.

20 Referring to FIG. 7A where the liquid crystal is presented as black, the level of the segment voltage VSEG maintains V0 in a first frame 1F where the level of the liquid crystal polarity inversion signal M is high. The level of the segment voltage VSEG maintains Vss in a second frame 2F where the level of the liquid crystal polarity inversion signal M is low.

25 Thus, in comparison to the waveform of the segment voltage VSEG in the conventional art, transitions between the voltage levels V0 and Vss decrease by 1/4. Therefore, power consumption due to transitions of the levels of the segment voltage VSEG can be reduced.

30 Referring to FIGS. 7B, 7C, and 7D, the non-selection voltage levels V2 and V3 are not used the same number of times. Referring to FIG. 7E where the liquid crystal is presented as white, transitions between the non-selection voltage levels V2 and V3

decrease by 1/4 compared to the waveform of the segment voltage VSEG in the conventional art. Therefore, power consumption due to transitions between the levels of the segment voltage VSEG decreases.

In addition, since the selection voltage levels or the non-selection voltage levels
5 are not the same number of times, cross talk does not occur in the liquid crystal.

FIG. 10 is a block diagram showing a driving method of an STN LCD driver according to another embodiment of the present invention.

FIG. 11 is a block diagram showing a driving method of an STN LCD driver further included in the method of FIG. 10.

10 Referring to FIGS. 10 and 11, in a driving method of an STN LCD driver 1000, a FRC signal is received, and it is determined whether a current driving method of an STN LCD driver is an nFRC method (step 1010).

The FRC signal has information on whether the driving method of the STN LCD driver is the nFRC method. In the nFRC method, one frame is comprised of n sub
15 frames.

The number of sub frames is counted (step 1020). If the number of sub frames is n, the liquid crystal polarity inversion signal M, which inverts the polarity of the liquid crystal, is generated (step 1030).

In the nFRC method, the liquid crystal polarity inversion signal M is generated in
20 each frame because one frame is comprised of n sub frames. If the liquid crystal polarity inversion signal M is generated, the polarity of the liquid crystal is inverted, and the liquid crystal is prevented from solidifying.

There may be various methods of generating the liquid crystal polarity inversion signal M in each frame. A method of generating the liquid crystal polarity inversion
25 signal M by using a counter, which counts the number of sub frames and generates the liquid crystal polarity inversion signal M every time when the number of sub frames counted is n, belongs to such methods. Since one frame is comprised of n sub frames in the nFRC method, counting of n sub frames is the same as counting one frame.

Data are received, and a segment voltage that drives a column electrode of the
30 STN LCD is generated in response to the level of the liquid crystal polarity inversion signal M (step 1040). A row selection signal is received, a com voltage that drives a

row electrode of the STN LCD is generated in response to the level of the liquid crystal polarity inversion signal M (step 1050). Steps 1040 and 1050 have been described in detail above. Therefore, their description will not be repeated.

As still another embodiment of the present invention, a driving method of an STN LCD driver using an nFRC method further includes inverting a polarity of the liquid crystal in each frame.

The STN LCD driver using the nFRC method inverts the polarity of the liquid crystal in each sub frame in order to prevent the liquid crystal from solidifying. However, in the present invention, the polarity of the liquid crystal is inverted in each frame, and the segment voltage level is transitioned in response to inversion of the polarity of the liquid crystal.

If the polarity of the liquid crystal is inverted in each frame, the selection voltage levels or the non-selection voltage levels of the segment voltage are used the same number of times. Thus, cross talk can be prevented from occurring, and power consumption can be reduced. The methods of generating the liquid crystal polarity inversion signal M in each frame are described above, therefore, description of such methods will not be repeated.

According to an STN LCD driver and a driving method of an STN LCD driver, the selection voltage levels or the non-selection voltage levels can be used the same number of times, and cross talk can be prevented from occurring in an LCD by reducing the number of transitions of segment voltage levels. In addition, power consumption can be reduced.

While the present invention has been particularly shown and described with reference to exemplary embodiments thereof, it will be understood by those of ordinary skill in the art that various changes in form and details may be made therein without departing from the spirit and scope of the present invention as defined by the following claims.